



ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

Best Available Copy

APPLICATION NO. 09/930607	CONT/PRIOR	CLASS 325 47	SUBCLASS 9	ART UNIT 32132	EXAMINER M. M. K. D.
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E Instruction processor and programmable logic device cooperative computing arrangement and method

PTO-2040
12/89

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
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